

PATENT
Attorney Docket No.: 728-208 (YOR9-2001-0270 US)

REMARKS

The Examiner objected to the use of inherent properties of pseudo-TFT gates, to distinguish the inventive claims over the prior art, without such properties being recited in the claims. The objected to passage from page 5, lines 19-23 of the Applicants' Amendment filed on August 7, 2003 reads as follows:

"In contrast, it is inherent in a pseudo-TFT gate as recited in Claim 12 as amended that both the source/drain and the body are contained in a polysilicon layer. Therefore, it would not be obvious for someone skilled in the art to use pseudo-TFT gates instead of the gates 48 disclosed by Nemati, because gates on polysilicon have different electron mobility characteristics from gates on a silicon substrate."

In response, as suggested by the Examiner, Claim 12 was amended to recite, "said transfer gate including a source/drain and body formed in a polysilicon layer".

The Examiner further objected to the recitation in Claim 12 of "wherein the top surface of said transfer gate forms a planar top surface of each said T-RAM cell" as being incorrect based on Figure 13 of the application. In response, Claim 12 was amended to recite, "wherein the top surface of said transfer gate forms a co-planar top surface of each said transfer gate of each said T-RAM cell".

Applicants submit that independent Claim 12 is believed to be in condition for allowance. Since Claims 14-17 are dependent from Claim 12, they are as well believed to be in condition for allowance for the same reasons: Claim 31 is cancelled without prejudice. Allowance is respectfully requested.

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Should the Examiner believe that a telephone conference or personal interview would facilitate resolution of any remaining matters, the Examiner may contact Applicants' attorney at the number given below.

Respectfully submitted,



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